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... GENERATION METHOD FOR SEMICONDUCTOR TEST APPARATUS (75) Inventors: Yoshihiro

Asano, Yokohama (JP) ... At the **translator** 61, there are provided compile means 611 for compiling ... semiconductor test apparatus, comprising: a first step of **converting** data necessary ...

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Deletion mutagenesis of stem cell factor defines the C-terminal sequences essential for its biological activity

... K Katayama, N Fujii, K Ozawa, S **Asano** - Biochemical and ..., 1992 - Elsevier

... b, Kanako Katayama b, Nobuyoshi Fujii b, Keiya Ozawa a and Shigetaka **Asano** b, a. ... For the sequential deletion of 3' endomSCF cDNA, at first, universal **translation** terminator (Pharmacia ... cDNA was inserted between BamHI and HindIII site of pBUT after **converting** SacI site of ...

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Fluctuation-free input buffer

N Masuda, M **Asano**, T Hayashii, H Tanaka ... - US Patent ..., 1987 - Google Patents

... Hoffman, "Positive Voltage **Translation** Circuit", IBM TDB, vol. ... V as a low level and about -0.9 V as a high level, VERSION INPUT CIRCUIT" filed by Michio **Asano** that is ... Vff = W2 (Voon, - Vssi - vi = vin- VTN / 55 60 65 That is, Vi becomes a voltage obtained by **converting** the level ...

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Cloning, nucleotide sequencing, and expression of an opine dehydrogenase gene from Arthrobacter sp. strain 1C.

[\(PDF\) from nih.gov](#)

... Y **Asano** - Applied and Environmental Microbiology, 1995 - aem.asm.org

... keto acids [2]. Such a secondary amine dicarboxylic acid structure is found in angiotensin-**converting** enzyme inhibitors ... Electronic mail address: **asano**@pu-toyama.ac.jp. ... sequence of the 2.1-kb PstI-XbaI fragment revealed an open reading frame, and **translation** of nucleotides 1 ...

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Level conversion input circuit

M **Asano**... - US Patent 4,563,601, 1986 - Google Patents

... 7, 1986 [54] LEVEL CONVERSION INPUT CIRCUIT [75] Inventors: Michio **Asano**, Kokubunji; Akira ... 6/1984 Wrathall 307/475 OTHER PUBLICATIONS Hoffman, "Positive Voltage **Translation** Circuit", IBM ... & Wands [57] ABSTRACT An input circuit is provided for **converting** an ECL ...

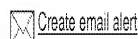
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Wide-range TOC instrument using plasma oxidation

... Y Yamamori, J Shilian, T **Asano** - US Patent App. 09/ ..., 2001 - Google Patents

... DE); Yasuo Yamamori, Longmont, CO (US); John Shilian, Longmont, CO (US); Tsutomu **Asano**, Oita (JP ... it would be desirable to employ plasma oxidation as the technique for **converting** TOC in ... the plasma is not in equilibrium with the vibration, rotation or **translation** energy of ...

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